

FIG. 1

201000-000000000000

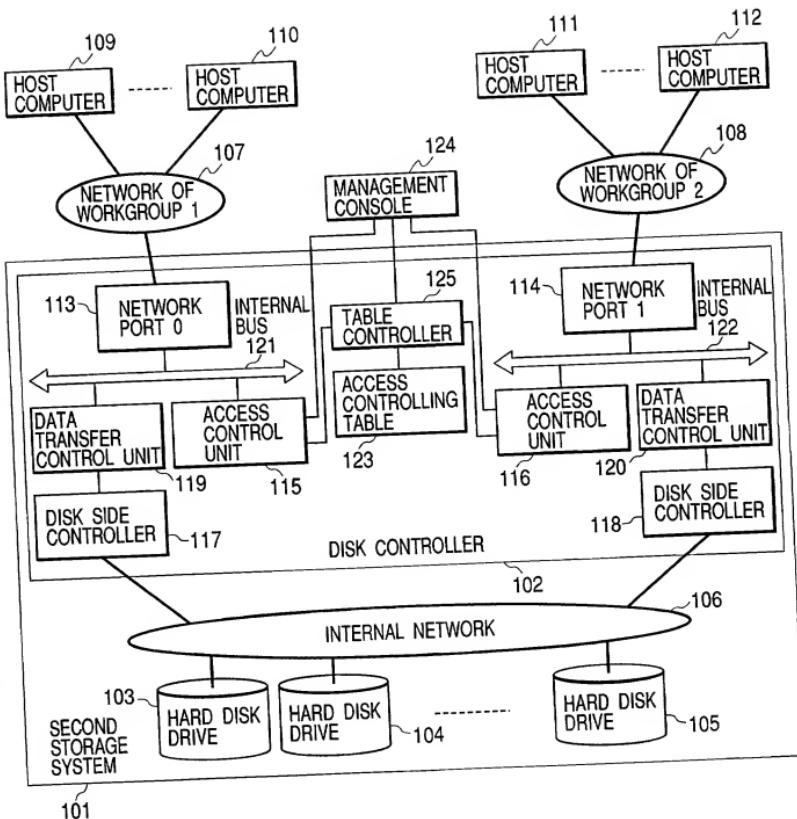


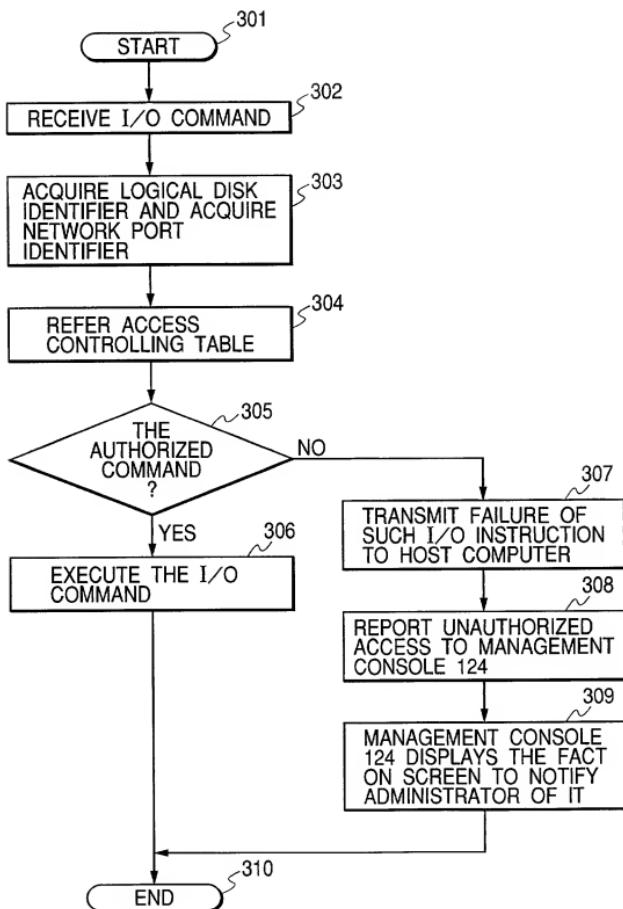
FIG. 2

	LOGICAL DISK 0	LOGICAL DISK 1	-----	LOGICAL DISK (n-1)
204	READ ENABLE WRITE ENABLE	READ ENABLE WRITE ENABLE	-----	—
205	NETWORK PORT 0	READ ENABLE	—	READ ENABLE WRITE ENABLE
	NETWORK PORT 1		-----	

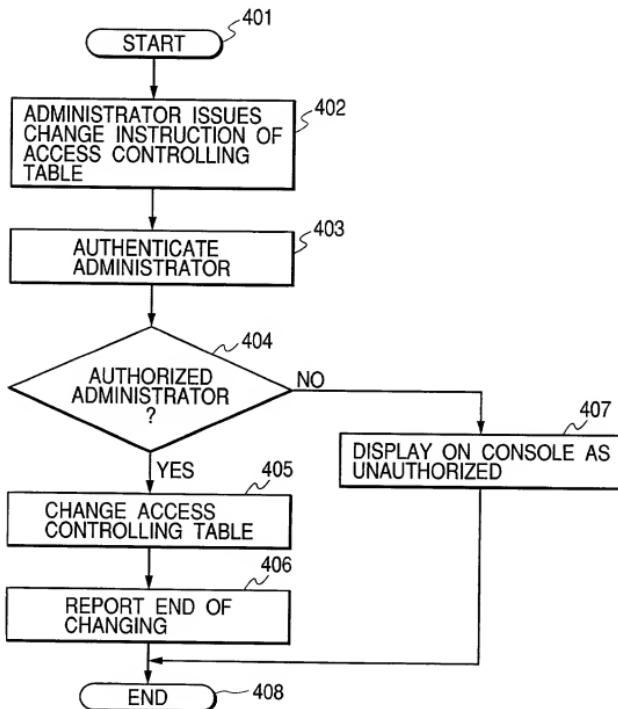
EXAMPLE

READ ENABLE : READ ONLY ENABLE
 WRITE ENABLE : WRITE ONLY ENABLE

FIG. 3



40034040-330102

FIG. 4

2020CO-0320010

FIG. 5

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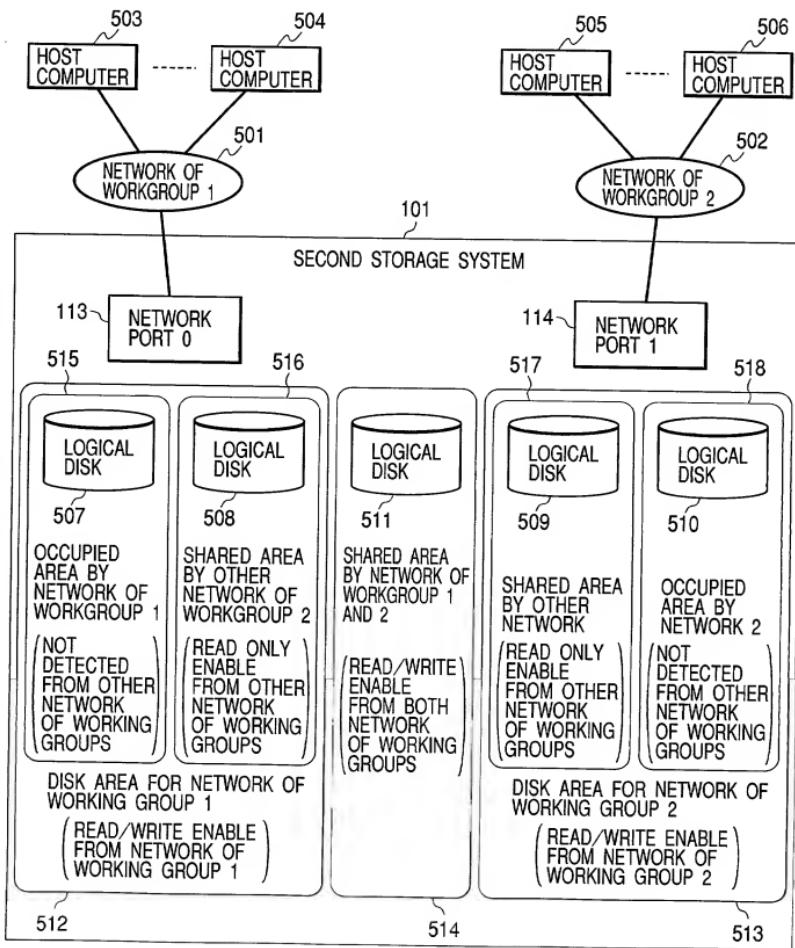


FIG. 6

The diagram illustrates a memory map with bus addresses 601, 602, 603, 604, 605, and 123. Address 601 connects to LOGICAL DISK 0. Address 602 connects to LOGICAL DISK 1. Address 603 connects to LOGICAL DISK 2. Address 604 connects to LOGICAL DISK 3. Address 605 connects to LOGICAL DISK 4. Address 123 connects to a component labeled '—' (dash). The memory map table below shows the state of logical disks and network ports.

	LOGICAL DISK 0	LOGICAL DISK 1	LOGICAL DISK 2	LOGICAL DISK 3	LOGICAL DISK 4
204	NETWORK PORT 0 READ ENABLE WRITE ENABLE	READ ENABLE WRITE ENABLE	READ ENABLE	—	READ ENABLE WRITE ENABLE
205	NETWORK PORT 1 —	READ ENABLE	READ ENABLE WRITE ENABLE	READ ENABLE WRITE ENABLE	READ ENABLE WRITE ENABLE

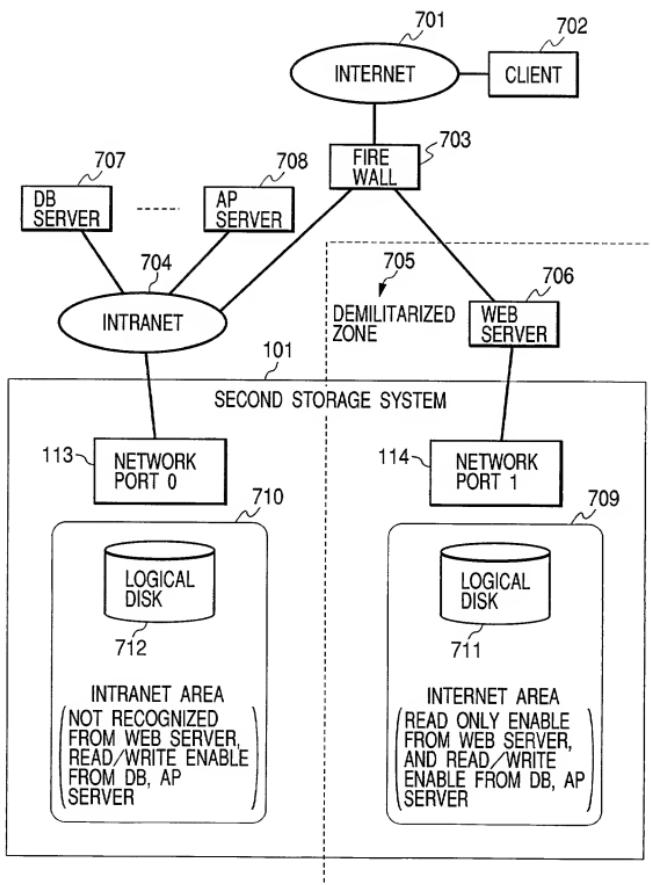
EXAMPLE

READ ENABLE : READ ONLY ENABLE

WRITE ENABLE : WRITE ONLY ENABLE

— : NOT DETECTED BY HOST

FIG. 7



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FIG. 8

	LOGICAL DISK 5	LOGICAL DISK 6
204	NETWORK PORT 0	READ ENABLE WRITE ENABLE
205	NETWORK PORT 1	READ ENABLE

EXAMPLE

READ ENABLE : READ ONLY ENABLE
WRITE ENABLE : WRITE ONLY ENABLE
- : NOT DETECTED BY HOST